The Danish all-in-one PID-Controller FB121 for Simatic S7

Force_UP

Force_DN

Variable

DELAY

Delay   2

Setpoint   2

Deadzone   3

H_Variable   1

L_Variable   1

DBW_P   4

DBW_I   7

DBW_D   5

Reverse   5

ON

Scantime   6

DELAY   6

SAMPLING CLOCK

RE-SCALING

PID CALCULATION

ACTION   8

ANALOG RAMPING

C - %

ADJUSTED - %

Traveltime

Max_B

Min_B

H_Switch

L_Switch

Delay_Var

UP

DN

PQW_C

High_C

Low_C

DBW_B

DBW_A
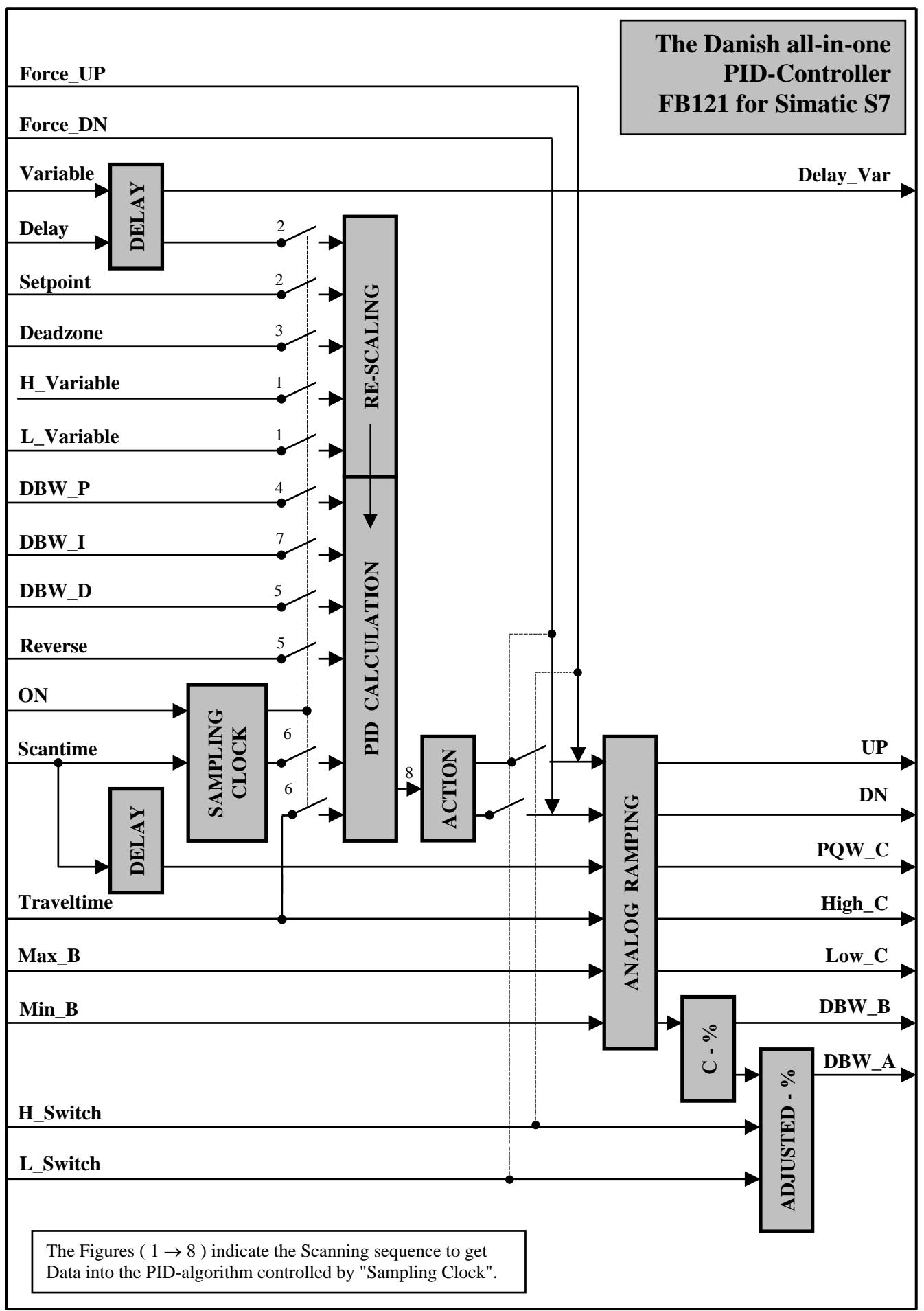
The Figures (1 → 8) indicate the Scanning sequence to get
Data into the PID-algorithm controlled by "Sampling Clock".

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